

COPY OF PAPERS  
ORIGINAL FILED



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Application No.: 09/779,096

) Date: August 29, 2002

Applicant: Shi-Tron LIN, et al.

) Examiner: Ori Nadav

) Group Art Unit 2811

Filed: 02/08/2001

) Our Ref: B-4101

For: "Output Buffer with Good ESD  
Protection"

) (618582-4/RPB:TGC)

RECEIVED  
#8  
SEP 16 2002  
TECHNOLOGY CENTER 2800  
Drwg Cor  
FJONES  
10-28-02

LETTER TO THE PATENT OFFICE OFFICIAL DRAFTSPERSON

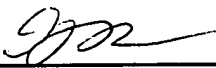
Hon. Commissioner of Patents and Trademarks  
Washington, DC 20231

Sir:

The Examiner objected to the drawings of the above referenced application. The Applicant hereby submits photocopies of Figures 1A and 1B with the proposed corrections shown in red ink. Please approve the proposed corrections. Upon receipt of a Notice of Allowance, the Applicant will present formal drawings including these corrections.


I hereby certify that this correspondence is being deposited  
with the United States Post Office with sufficient postage as  
first class mail in an envelope addressed to: Commissioner of  
Patents and Trademarks, Washington, D.C., 20231 on  
August 29, 2002  
(Date of Deposit)

Troy Guangyu Cai  
(Name of Person Signing)

  
(Signature)

8/29/02  
(Date)

Respectfully submitted,

  
Troy Guangyu Cai  
Attorney for Applicant  
LADAS & PARRY  
5670 Wilshire Blvd., Suite 2100  
Los Angeles, California 90036  
(323) 934-2300